



परीक्षा नियंत्रक का कार्यालय  
अनुसंधान इकाई  
अलीगढ़ मुस्लिम विश्वविद्यालय

Office of the Controller of Examinations  
Research Unit  
Aligarh Muslim University

امتحانات کے کنٹرولر کا دفتر  
ریسرچ یونٹ  
علی گڑھ مسلم یونیورسٹی

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0571-2700920 (Extension 4262)

D.No. **05/372**/COE/RU

Dated: **21.08.2025**

Reference this office Notice D.No.05/347/COE/RU dated 26.07.2025

**ADMISSION TEST AND PRESENTATION-CUM-INTERVIEW SCHEDULE  
FOR ADMISSION TO PH.D PROGRAMME  
UNDER VISVESVARAYA PH.D SCHEME FOR ELECTRONICS AND IT  
PHASE-II-2025-2026**

S.No	Faculty	Date of Test	Date of Presentation - Cum - Interview
1.	Engineering & Technology	24.08.2025	25.08.2025
2.	Science	24.08.2025	25.08.2025

1. The Ph.D. Admission Tests and Presentation-Cum-Interview will be held at **Aligarh** only.
2. The detailed schedule is given in **Annexure-I**.
3. Admit Cards (for eligible candidates) will only be downloadable from the Controller's website **[www.amucontrollerexams.com](http://www.amucontrollerexams.com)** on **22.08.2025**

Controller of Examinations

**Distribution:**

1. Deans of Faculty of Science/Engineering and Technology
2. Proctor / Dean, Students' Welfare
3. Chairmen/Coordinator as stated above
5. P.S. to Controller of Examinations
6. All D.C. /A.C/ In-charge Computer Cell
7. MIC (PRO)
8. Assistant Registrar, Vice-Chancellor's Secretariat
9. Guard file.



**Schedule of Ph.D Admission Test and Presentation-Cum-Interview**  
**Under Visvesvaraya Ph.D Scheme for Electronics and IT Phase-II-2025-2026**

Particulars	Date	Timing	
Written Test*	24.08.2025	Section A 10:00 AM to 11:00 AM	Section B 11:15 AM to 12:15 PM
Presentation-Cum-Interview**	25.08.2025	08:30 AM onwards	

**Note: - Candidates have to appear in both Section- A & Section -B of the Admission Test**

**\* Venue of the Written Test will be mentioned on Admit Card**

**\*\*Venue of Presentation Cum Interview:**

Discipline	Venue
Computer Science	Department of Computer Science
Electronics Engineering	Department of Electronics Engineering
Computer Engineering	Department of Computer Engineering